1. System Reliability

Quality is never an accident. It is always the result of intelligent effort.—John Ruskin

A typical design project in your academic career may never leave the confines of a laboratory. However, in industry, engineers develop systems that are used by the public at large, and issues beyond the functionality, such as reliability, safety, and maintainability become important factors in the success of the design. Over the past 20 years, industry has made a great shift to address reliability through the adoption of processes such as Quality Functional Deployment (QFD), Six-Sigma, and Robust Design. While other chapters have addressed some elements of these processes, the objective of this chapter is to examine system reliability. Reliability attempts to answer the question of how long a system will operate without failing. Answering this question has inherent uncertainty and requires the use of probability and statistics. This chapter presents a review of basic probability theory and applies it to estimate the behavior of real-world devices. Reliability at the component and system levels is considered.

Learning Objectives

By the end of this chapter, the reader should:

* Have a familiarity with the basic principles of probability and understand how they apply to reliability theory.
* Understand the mathematical definition and meaning of failure rate, reliability, and mean time to failure.
* Understand how to determine the reliability of a component.
* Understand how to derate the power of electronic components for use under different operating temperatures.
* Understand how to determine the reliability of different system configurations.

**DILBERT® by Scott Adams**



### Figure 8.1 Dogbert’s Six Sigma Program. (Dilbert © United Feature Syndicate. Reprinted by permission.)

* 1. Probability Theory Review

Probability theory provides a formal framework to study chance events. It is a powerful tool for modeling engineering systems and is a requisite for reliability estimation. Although this section provides a review of some important concepts from probability, it is assumed that the reader is versed in the basics of probability theory.

In order to apply probability, some general definitions are examined first. An *experiment* is the process of measuring or quantifying the state of the world. The particular outcome of an experiment is an *event* (),while the *event space* (*E*) is the set of all possible outcomes of the experiment. For example, consider an experiment where a six-sided die is rolled. The experiment is rolling the die and observing the outcome, the event is the particular outcome observed, and the event space for the experiment is the set *E* = {1,2,3,4,5,6}. The outcomes do not have to be numerical values. Another example experiment is tossing a coin, in which case the event space is *E* = {Heads, Tails}. Both are examples of a discrete event space because there are a finite number of experimental outcomes. In a discrete event space, the union of all the possible experimental outcomes defines the event space. If  is the ith event in a discrete event space, then the event space is given by the union

. (1)

The probability of an event indicates how likely it is for an event to occur. This is quantified by the probability operator, *P()*, that assigns to each event a real number between 0 and 1. The probability is the percentage of times that an event would occur if the experiment were repeated an infinite number of times (the Law of Large Numbers). Two of the three fundamental axioms on which probability theory is built are

 (2)

. (3)

The first axiom indicates that all probabilities are non-negative, while the second is a restatement of the event space definition—the outcome of an experiment must be an element of the event space. Armed with these definitions and axioms, some important concepts from probability are now examined.

### Probability Density Functions

Not all event spaces are discrete as in the case of rolling a die or flipping a coin. Consider an experiment where the objective is to measure temperature. Clearly, such a measurement requires a variable having a continuous range of possible values. A random variable is defined as the outcome of an experiment that has a continuum of possible values. Random variables have a mathematical function known as the *probability density function* (PDF) associated with them, which when integrated, yields the probability of a range of events. A PDF is typically denoted as , where *X* takes values over the event space. Standard notation identifies random variables using upper case variables as the subscript for the PDF. The variable inside the parentheses is a lowercase dummy variable that does not have to match the random variable, but typically does. A question that the PDF allows us to ask is “*What is the probability that a random variable is in some range?*” Consider the case where the objective is to determine the probability that the random variable *X* lies between two values *a* and *b*. Written using the probability operator, this is indicated as . It is determined from the PDF as follows

. (4)

Conceptually, this probability represents the area under the PDF between the two limits of integration as shown in Figure 8.2.



**Figure 8.2** A probability density function. The area under the curve represents the probability that the random variable *X* lies in the interval [*a,b*].

Let’s examine a few more important properties of probability density functions. The first, which is analogous to (3), indicates that the probability of the event space occurring is equal to one. This is known as the normalization property and it is expressed as

. (5)

Another interesting result is obtained by trying to determine the probability that a random variable takes on an exact value, for example . That is determined from the integral

. (6)

This is a somewhat counterintuitive result—it indicates that the probability a random variable can take on a particular value is zero. Does this make any sense? Consider an experiment where the objective is to measure a voltage value for a random variable *V*. Now consider the question, “*What is the probability that the result of a voltage measurement equals π (the irrational number) volts?*” In practice, this question is impossible to answer because the precision required of the meter is infinite and contrary to its construction. So the mathematical and practical results are in harmony. There is a way around this dilemma, which is to determine the probability that the random variable is within a small range about the target value as follows

. (7)

This means that the probability a random variable is within a small range about a given value is approximated by the product of the PDF evaluated at the value and the size of the range.

### Mean and Variance

Two useful and well-known statistics that are determined from the PDF are the mean () and variance (). They are found from the PDF as follows

 (8)

. (9)

The *mean* is analogous to the center of mass of the PDF; it is also known as the average value. The *variance* is the average of the squared difference between the mean and the values of the PDF, where the squared term ensures that a positive difference is taken. The square root of the variance is known as the standard deviation ().

### Common Probability Density Functions

There are many PDFs available for describing the seemingly random variations in the behavior of observed systems and phenomena. In this section, three common PDFs (normal, exponential, and uniform) are presented.

### The Normal Density

The most common density function encountered in the physical sciences and engineering is the normal density***.*** Many population variations can be described by a normal density. For example, the resistance values of a large batch of 2.2k ohm resistors would likely follow a normal density. The normal density is defined as

. (10)

The mean, *µ*, and standard deviation, *σ*, are part of the definition of the PDF and used to alter the shape of the density to suit the particular need. The normal PDF is plotted in Figure 8.3. Varying *µ* allows the overall function to be shifted along the x-axis, while increasing *σ* spreads (or flattens) the function out. Calculating probabilities from the normal density can be done (although it takes a bit of work mathematically) so they are usually computed from something known as the Cumulative Distribution Function that is presented shortly.



**Figure 8.3** A normal density function with the mean (*µ*) and standard deviation (*σ*) shown.

### The Uniform Density

The uniform density, plotted in Figure 8.4, models the outcome of an experiment where all outcomes are equally likely. Mathematically, the PDF for a uniform density is given by

, (11)

where *a* and *b* are selected to meet the demands of a particular problem.



**Figure 8.4** The uniform density on the interval [*a,b*].

### The Exponential Density

Exponential densities are often utilized to model time dependent functions, such as inter-arrival times between data packets in communication systems. As shown later, the exponential density also describes the behavior of component failures as a function of time. The mathematical description of an exponential density is

. (12)

The PDF is characterized by the parameter ** which affects the shape of the curve as demonstrated in Figure 8.5.



**Figure 8.5** The exponential density for two different *λ* values.

### Cumulative Distribution Functions

An important class of questions can be phrased as, “*What is the probability that a random variable X is less than value a?"* For example, the objective might be to determine the probability that an electronic component will malfunction within two years. Returning to the first question, it is clear that the goal is to determine the probability , which is found by integrating the PDF. This result is generalized by allowing the upper limit of integration to take on an arbitrary value that spans the range of the random variable. This produces a new function, known as the *Cumulative Distribution Function* (CDF), which is the integral function of the PDF and is defined as

. (13)

* 1. Reliability Prediction

Our main interest in the study of probability stems from the desire to quantify the reliability of a system. The following is a formal mathematical definition of ***reliability***.

**Definition**: Reliability, *R(t)*, is the probability that a device is functioning properly (has not failed) at time *t*.

In order to determine *R(t),* it is necessary to first introduce some related mathematical entities and their meanings. The ***failure rate***, *(t),* of a device is the expected number of failures per unit time. The failure rate is measured by operating a batch of devices for a given time interval and noting how many fail during that interval. A typical graph of failure rate versus time has the bathtub shape shown in Figure 8.6. The high initial failure rate is a result of manufacturing defects often referred to as infant mortality. Consequently, many manufactures will “burn-in” devices at the factory, so that if they fail, they do so before being sold. After the infant mortality phase, devices enter a phase of constant failure rate, where*(t)=*, known as the service life. Estimates for ** are determined empirically by testing a large number of components. They are usually expressed as a unit failure per a given number of hours, for example . After some period of time, devices start to wear-out and the failure rate increases. This usually happens as a result of mechanical wearing with age and use. Properly designed electronic devices will not have a wear-out region, instead continuing on at a constant failure rate. This applies only to the electronic devices themselves, not necessarily to complete systems that will likely contain mechanical devices.



**Figure 8.6** Failure rate as a function of time, also known as the bathtub curve.

In addition to failure rate, a PDF for the *failure time* of the device, **, is defined, where the random variable is time *T*. This function allows the question to be asked “*What is the probability that a device will fail between time t1 and t2?*” It is important to note the difference between *(t)* and **. The failure rate tells us the average rate that a collection of identical devices will fail at a given time *t*, while ** is a PDF used to determine the probability that a given device will fail within a specified time period. A CDF for ** is determined as

. (14)

*F(t)* answers the question “*What is the probability that the device has failed by time t?*” and it is also known as the ***failure function***. Take a few seconds to go back and review the definition of *R(t)*. It is clear that *R(t)* is directly related to *F(t)* and is its complement. The relationship between the two is

. (15)

Since *F(t)* is a CDF, it increases monotonically from an initial value of 0 to a maximum value of 1 as time goes to ∞ as shown in Figure 8.7. Conversely, *R(t)* starts at a value of 1 at time zero and decreases monotonically to a value of 0.



**Figure 8.7** Example reliability and failure functions.

Since *(t)* represents data that is measured empirically, it is useful to establish a relationship between *(t)* and the ultimate goal of reliability, *R(t)*. To do so, a relationship between *(t)*, *R(t)*, and ** is established as follows. Consider a small period of time between *t* and **, and determine the probability of device failure during this period. From the approximation developed in (7), this probability is given by

. (16)

How is this probability related to *R(t)* and *(t)*? *R(t)* provides the probability that the device is working at time *t* and *(t)*gives the probability that the device will fail at time *t*. The product of *R(t), (t)*, and Δ*t* gives the same probability of failure in (16)

. (17)

Equating (16) and (17) provides the desired relationship between the three quantities

, (18)

that is fundamental in establishing the connection between *R(t)* and *(t)*. However, the PDF ** needs to be eliminated from (18). This is accomplished through its relationship to the CDF *F(t)*, and thus *R(t)*, as follows

. (19)

Equating this result with (18) produces

. (20)

Integrating both sides gives

 (21)

and solving for *R(t)* produces the final result for reliability as a function of*(t)*

. (22)

During the service lifetime phase, the failure rate is constant, which simplifies to

. (23)

This important result is now applied in Example 8.1.

Example 8.1 Transistor reliability.

***Problem:*** Consider a transistor with a constant failure rate of . What is the probability that the transistor will be operable in 5 years?

***Solution:*** This solution is found using the reliability function for a constant failure rate in (23) as follows.



= 95.7%.

### Mean Time to Failure

The ***mean time to failure*** (MTTF) is a quantity which answers the question, “*On average how long does it take for a device to fail?*” From its definition, it is apparent that the MTTF is the mean value of the random variable *T* (failure time). It is determined from the PDF and the definition of the mean in (8) as follows

. (24)

At this point the form of the PDF for ** is not known, but it can be found from (19) since it is the negative derivative of *R(t).* Assuming the form of *R(t)* found in (23) for a constant failure rate gives

. (25)

This means that under the condition of a constant failure rate, the failure PDF follows an exponential density. The MTTF is found from ** via integration by parts to be

. (26)

This makes intuitive sense because *λ* is the expected number of failures per unit time for a device. Consequently, the reciprocal of *λ* is the expected time between failures or MTTF. Let’s consider a few examples.

Example 8.2 Transistor MTTF.

***Problem:*** Consider the transistor in Example 8.1. (a) Determine the MTTF, and (b) the reliability at the MTTF.

***Solution:***

(a) From (26) the MTTF = 

= 114 years.

(b) From (23) the reliability at 114 years is



= 36.8%.

This is a bit counterintuitive. Although the average time between transistor failures is 114 years, an individual transistor has only a 36.8% chance of surviving to 114 years. It would seem logical that the reliability at 114 years should be 50% and that the transistor would have a 50-50 chance of failing. This would be true if **were symmetric about its mean, but that is not the case for the exponential density.

**Example 8.3** Human lifespan estimation.

***Problem:*** Data shows that for a 30 year old population, the failure (death) rate is constant with approximately 1.1 deaths per 1000 people per year. Given this data, estimate the MTTF of humans.

***Solution:*** In order to find MTTF, λ is needed. From the information given it is



From this MTTF is computed as

 = 909 years!

While great news for those of us seeking longevity, this calculation is clearly wrong since the upper limit on human lifespan is empirically known to be about 120 years. Why is this so? Serious problems arise if *R(t)* is used in situations where the underlying assumption is invalid. The results in (23) and (26) apply only if the failure rate is constant. Although that is nearly true for people in their 20s and 30s, it is not true as people age. People do wear out and the failure rate increases with age.

### Failure Rate Estimates

The overriding objective of this chapter is to estimate the future behavior of devices that are used in electrical and computer systems. The particular behavior of interest is the state of a device’s functionality—the reliability, is it working or has it failed? Equation (23) indicates that it is fairly straightforward to determine reliability, if the failure rate (**) is known and is constant. One question to consider is what factors influence the failure rate of a device. Many of us probably have had experiences in the laboratory where we have caused devices to fail by subjecting them to conditions outside of the normal operating bounds, notably excessive current, power, or heat. In those cases the devices probably failed, or burned up, due to the operating conditions outside of the allowed bounds for the device. However, even when operated within the allowable norms of a device’s operating conditions, variations in factors such as power, operating voltages, and temperatures impact **.

The United States Military has kept copious records of device failures in the field and the conditions under which they operated. These records are synthesized in a handbook entitled Reliability Prediction of Electronic Equipment [MIL-HDBK-217F] that provides failure rates for various analog and digital components, along with adjustment factors to account for operating conditions, the environment, and device quality. Categories of devices included in the handbook are switches, fuses, diodes, optoelectronic devices, and microelectronic devices (op amps, logic devices, microcontrollers, microprocessors). The handbook was last published in 1991 and has been discontinued, but it is still widely accepted and used. Bellcore (subsequently Telcordia) has developed newer models [Tel96] based upon MIL-HDBK-217F that were updated to better predict the reliability of components. MIL-HDBK-217F is used here since it is freely available in the public domain.

Failure rates for resistors, capacitors, transistors, and integrated circuits from MIL-HDBK-217F are included in Appendix C. For each device, a base failure rate is given, *λb*, and multiplied by a number of adjustment factors, denoted by the symbol , to estimate the device failure rate *λ*. Each adjustment factor has a unique subscript and their values are found from tables or equations in the handbook.

For example, consider the low frequency field effect transistor in Appendix C. The overall failure rate is given by the equation . is the base failure rate that is directly read from a table, is a temperature factor which is computed from an exponential equation (be careful to use the junction temperature as indicated in Appendix C),  is an application factor that depends upon how the device will be used,  is a quality factor, and  is an environmental factor. The quality factor table lists some strange names and values from 0.7 to 8.0. The quality factor describes the level of burn-in and screening each device receives before leaving the factory. Joint Army/Navy (JAN, JANTX, and JANTXV) quality factors are the highest standard, and are usually required only for space vehicles. That individual attention to burn-in means that JAN parts are expensive and most JAN devices have passed their infant mortality phase before leaving the factory. When determining failure rate for a device from a table, it is common practice to always round parameters or values pessimistically so that the evaluation is a worst case analysis of its performance. That way the device should perform with a higher reliability when embedded into a system, hopefully causing only pleasant surprises in operation. Finally, the factor  is based upon the different operating environments that are identified in Appendix C. Example 8.4 demonstrates the application of the MIL-HDBK-217F standard for reliability estimation.

In summary, it is possible to estimate the reliability of devices if the failure rate is known and it is constant. The US Military and Telecordia handbooks provide guidance for estimating failure rates, and thus the component reliability. It must be kept in mind that they are estimates and not guaranteed to predict the exact performance. It is also apparent that this can become a rather time-consuming process if there are many components in a system, and thus the use of reliability software packages may be warranted.

**Example 8.4** Reliability estimation using the MIL-HDBK 217F.

***Problem:*** Consider the circuit below that contains a bipolar junction transistor (BJT). This electronic circuit is a simple digital logic inverter. When the input voltage  is 0, the BJT is off, no current flows through any branches of the device, and the output voltage, , is 5V. When the input is 5V (high) the BJT goes into saturation due to the high base current (low ), producing a 50mA collector current, a large voltage drop across RC, and an output voltage close to 0V. The average collector current for the two states is 25mA, producing an average power of 125mW (25mA×5V). Assume that the circuit is used in a missile launcher, the ambient temperature is 25°C, and that JANTX quality parts are used. Determine the MTTF and reliability for the 2N3904 BJT (a low power, low frequency BJT) in 20 years.



***Solution:*** The objective of this problem is to determine the failure rate, from which the MTTF and reliability are estimated. From the MIL-HDBK-217F data in Appendix C, the failure rate is



The base failure rate is given directly as .  is the temperature factor and its value is determined from the relationship

.

where *TJ* is the junction temperature. As indicated in Appendix C, it is computed as



The thermal resistance, , is read from the 2N3904 datasheet (Appendix D) and will be examined in more detail shortly. The temperature factor is



 is an application factor (switched or linear amplification), and the value for the switched logic inverter is .  is a power rating factor that is computed based upon the maximum rated power dissipation of the 2N3904 (625mW from the component datasheet in Appendix D) as follows

.

 is a stress factor that is computed from the ratio of the maximum collector-emitter voltage over the maximum rated value of the device. In this circuit, the maximum value of  is 5V (when the device is off and no current flows), while the maximum rated value of  from the 2N3904 datasheet is 40V.



 is the quality factor, and based upon the fact that a JANTX part is used .  is the environmental factor, and for the missile launch application, .

All of this is brought together to compute the failure rate. The product of the adjustment factors is computed as



This allows estimation of the MTTF and requested reliability

 = 71,804 years.



= 99.97%.

In conclusion, the BJT is estimated to be highly reliable in 20 years.

### Thermal Management and Power Derating

One of the quantities computed in Example 8.4 was the junction temperature, *TJ*, which was computed from the power dissipated in the device and a quantity known as thermal resistance, *θ*. It is important to understand this in more detail, since it impacts the reliability of microelectronic devices. Furthermore, if the junction temperature exceeds a certain value, the device will fail. Thus, thermal management issues need to be taken into account. We start with a physical model in Figure 8.8 (a), which has a junction (the integrated circuit or device), enclosed by a case (the packaging of the device), surrounded by ambient environmental conditions. In part (b) a heat sink is included which aids in thermal transfer. Each element has associated with it a quantity known as thermal resistance that measures the ability of that particular element to transfer heat to another element. A result from heat transfer for electronics is that changes in temperature (Δ*T*) are proportional to the product of power dissipation () and the thermal resistance. This relationship is

. (27)

It is similar to Ohm’s Law where the change in temperature, power, and thermal resistance (units = °C/W) are analogous to voltage, current, and electrical resistance respectively. The total thermal resistance between two elements, such as between ambient and the junction, is the sum of all thermal resistances between them. In the case with no heat sink, this produces a junction to ambient resistance of , while in the case with a heat sink, . In the case of the heat sink,  is replaced by the sum , which has a lower combined thermal resistance and greater ability to dissipate heat.

 

(a) (b)

**Figure 8.8** Physical model of microelectronic devices with thermal junctions and temperatures indicated. (a) Device inside casing. (b) Device inside casing with a heat sink added.

This Ohm’s Law type of relationship means that thermal transfer can be modeled using familiar resistive circuits as shown in Figure 8.9. Based upon this circuit model, the temperature can be found at different points from the thermal resistance and power dissipation. Most importantly the junction temperature is found as

. (28)

 

(a) (b)

**Figure 8.9** Resistive models for thermal transfer in microelectronic devices. (a) Device with no heat sink. (b) Device with a heat sink added.

Let’s now apply these results. Manufacturer datasheets typically identify the absolute maximum power dissipation and note that the device should be derated if operated at ambient conditions above room temperature (). The datasheets also supply a maximum junction temperature for the device. It is clear from the resistive model that, for a fixed power dissipation, the junction temperature increases along with ambient temperature. If the maximum junction temperature is exceeded, the device will be destroyed. Another way to look at this is that as ambient temperature increases, the maximum amount of power a device can dissipate decreases. This decrease in maximum power dissipation is known as ***derating***. From (28) the maximum power that can be dissipated in a device at a given ambient temperature is

 . (29)

From this relationship, a power derating curve is plotted in Figure 8.10 showing the maximum power versus ambient temperature. Example 8.5 demonstrates the application of this to the inverter in Example 8.4.



**Figure 8.10** Typical power derating curves.

**Example 8.5** Power Derating for the Inverter Circuit.

***Problem:*** Assume the circuit in Example 8.4 is operating at an ambient temperature  and that no heat sink is used. (a) Determine the derated power and if the design is within the manufacturer’s limits for power dissipation at this temperature and, (b) re-compute the reliability at 20 years based upon this elevated operating temperature.

***Solution:***

(a) From the manufacturer datasheet in Appendix D, the 2N3904 BJT has a thermal resistance of  and a maximum junction temperature of 150°C. From this the maximum power is computed from (29) as

.

The derated, or maximum, power at this temperature is 150mW. Clearly, the 125mW of power dissipated as determined in Example 8.4 for the BJT is within this derated limit.

(b) To compute the failure rate the junction temperature and are re-computed.



With this new value, the value of , and the reliability is reduced slightly to 99.88%. Note, however, the junction temperature is quite high at 145°C and further increases in temperature would likely destroy the device.

### Limits of Reliability Estimation

It must be kept in mind that the reliability estimates are just that, estimates, and there are limitations in their use. First, realize that the failure rate data comes from accelerated stress tests, where devices are put under stress beyond normal operating conditions, and from these the failure rates are estimated. (Nobody sits around waiting 20 years for the devices to fail!) The tests are based upon mathematical models for the failure rate and the device lifetime. Secondly, there are other factors that influence reliability that are not addressed by *λ*, such as the manufacturing processes used, the quality of manufacturing technologies, shock, and corrosion. Part of the value of reliability estimation is for comparative purposes when evaluating different design options. Applying these methods forces the designer to consider the operating conditions and factor them into the design.

* 1. System Reliability

The previous section focused on determining the reliability of a single device. It is natural to ask, “*How can the reliability of a system consisting of many devices be determined?*” In order to derive the overall reliability of a multi-component system, it is necessary to take into account the overall system structure.

### Series Systems

Consider the inverter circuit in Example 8.4—failure of any one component in the circuit would lead to the failure of the overall system or circuit. Conceptually, a system in which the failure of a single component (or subsystem) leads to failure of the overall system is known as a ***series system***. Figure 8.11 shows a block diagram of a series system composed of boxes , , ...,  that represent the components, or the subsystems, of a larger system.



**Figure 8.11** A series system consisting of components, or subsystems , , …, .

To compute the overall reliability of a series system, , it is assumed that the failure of subsystems or components are independent events. The system is operable only if subsystems  and  … and  are all simultaneously operating. Therefore, the probability of the overall system operating is given by the product of reliabilities for all of the subsystems as follows

. (30)

It is important to remember that failures are assumed to be independent events, just as flipping a coin twice is considered two independent events. The overall system reliability is less than or equal to that of any single subsystem, since all reliability values are ≤1. Thus  decreases as the number of subsystems increases. Assuming a constant failure rate for all system components gives the following result for the overall system reliability

. (31)

This leads to a series system failure rate and MTTF of

. (32)

Example 8.6 revisits the inverter problem where the failure rates of all components are considered for system reliability estimation.

**Example 8.6** Inverter circuit reliability.

***Problem:*** For the system in Example 8.4 estimate (a) the overall system reliability in 20 years, and (b) the MTTF. Assume room temperature and that ¼ watt fixed composition resistors are used.

***Solution:***

(a) Conceptually this is a series system—if any of the individual components fail, then the overall system will fail. That means that failure rates for the two resistors are needed in addition to the value previously computed for the transistor. They depend upon the power dissipated in each resistor, which is 125mW and 0.9mW for the collector and base resistors respectively. The failure rate for a fixed composition resistor from MIL-HDBK-217F is



For the collector resistor, RC, the base failure rate is computed as



The *S* term is the ratio of power dissipated to the maximum power rating. The values , , and  are directly read from tables. Thus the overall failure rate for the collector resistor is

.

The process for the base resistor, RB, is similar, and results in

.

The total failure rate is given from (32) as





= 96.3%.

Since resistors are pretty reliable devices, the overall system reliability decreases only a small amount relative to that of the BJT itself.

(b) The MTTF is given by  which in this case is 531 years.

### Parallel Systems

From (30) it is clear that as more components are added to a series system, the reliability decreases. It is natural to ask if the reliability can be increased. The use of redundancy gives us a method to answer in the affirmative. A design has ***redundancy*** if it contains multiple modules performing the same function where a single module would suffice. By its very nature redundancy allows improperly functioning modules to be switched out of the system without affecting its behavior. With redundancy the overall system functions correctly when any one of the submodules is functioning. Figure 8.12 shows a simplified view of a ***parallel system*** with subsystems , , …, .



**Figure 8.12** A parallel, or redundant, system consisting of subsystems , , …, 

In order to compute the reliability of a parallel system, note that a parallel system functions correctly when  is functioning correctly, or  is functioning correctly, … or  is functioning correctly. It would be nice if it were possible to write an equation stating that *,* where + is the logical OR operator. Unfortunately, there is no direct way to realize the OR operation in probability theory. This is resolved by working with failure function, *F(t)*, instead. The probability that the system will fail by time *t*, , is equal to the probability that subsystem  will fail and  will fail and …  will fail. This probability is expressed mathematically as

. (33)

The overall system reliability of the parallel system is found from this as

 . (34)

As more redundant components are added to a parallel system, additional  terms are introduced into the product term. This decreases the value of the product, hence the overall system reliability is increased as more redundant systems are added.

In order for a parallel system to work, a mechanism must be in place to monitor each of the subsystems to make sure that they are operating correctly. Developing circuits to detect failures and control the switching between subsystems can be complex and is not considered here. Special care must be paid to the switching circuit itself, as malfunction of this circuit could lead to an overall system failure. An example of parallel system reliability is given in Example 8.7.

**Example 8.7** Reliability of a Redundant Array of Independent Disks (RAID).

***Problem:*** In a RAID, multiple hard drives are used to store the same data, thus achieving redundancy and increased reliability. One or more of the disks in the system can fail and the data can still be recovered. However, if all disks fail, then the data is lost. For this problem, assume that the individual disk drives have a failure rate of . How many disks must the system have to achieve a reliability of 98% in 10 years?

***Solution:*** The reliability of a parallel system with redundancy is given by (34). Since all of the disks are identical, the expression simplifies to

.

In order to achieve this reliability, n=8 disks are required. The reliability of each individual disk is low at 42%, but using redundancy, the overall system reliability is quite high.

### Combination Systems

Many real systems do not fit neatly into either parallel or series reliability models as shown in Figure 8.13. Rather, they may be a combination of the two, and such systems will be referred to as combination systems. One way to determine the reliability of a combination system is to utilize the results obtained for series and parallel systems in (30) and (34). The system network is reduced by combining parallel subsystems into a single block, whose reliability is given by (34), while series subsystems are reduced to a single block whose reliability is given by (30). This is conceptually analogous to combining series and parallel resistances in electrical circuits. The network is continually reduced until only a single block remains whose reliability is known from all of the subsystem combinations.

To illustrate this, consider the system in Figure 8.13. To determine the reliability, start by combining the three parallel systems , , and , whose reliability is determined by application of (34) to be . The result is then combined with  in series to give the overall system reliability, . The chapter concludes with Example 8.8 that addresses combination system reliability.



**Figure 8.13** A combination series-parallel system. S2, S3, and S4 are redundant parallel systems.

**Example 8.8** Combination system reliability.

***Problem:*** Consider the system shown below with the following reliabilities at a fixed time *t*, . Determine the reliability that subsystems  and  must have so that the overall system reliability is greater than 95%.



***Solution:*** The parallel systems can combined into single systems whose reliabilities are



They are combined in series to give the overall system reliability

.

Substituting values and assuming  gives

.

Solving for the reliabilities gives the final result

.

This example demonstrates the power of redundant systems.  and  have somewhat low reliabilities relative to the overall system goal, but the reliability of the parallel combination of  and  is 96%. It requires a reliability for systems 3 and 4 of 90%, while the combined reliability of systems 3 and 4 is 99%.

* 1. Summary and Further Reading

This chapter presented the basics of probability theory and methods for estimating the reliability of components and systems. Failure rate is an important quantity that is determined empirically and provides the rate of failure over the lifetime of a component or system. A mathematical definition of reliability was derived from this quantity, which takes a simple exponential form in the case of a constant failure rate. This was applied to estimate the reliability of single components, particularly using failure rates from MIL-HDBK-217F. Issues of thermal transfer and power derating were considered. Reliability estimation was extended to more realistic systems consisting of multiple components in series and parallel forms. The use of redundancy with parallel systems to increase the overall system reliability was addressed.

There are plenty of good textbooks available on the probability theory, if it is necessary to study probability theory further. The book Practical Reliability of Electronic Equipment and Products [Hna03] provides detailed coverage for electrical systems reliability. It includes factors not considered here such as thermal management on printed circuit boards, procurement practices, and electromagnetic interference. Two excellent articles that demonstrate the application of design for reliability and redundancy for an embedded system application are by George Novacek in Circuit Cellar magazine [Nov00, Nov01].

* 1. Problems
  2. Consider a random variable that obeys a uniform density and varies from 2 to 5. (a) Determine the mean and variance of the random variable. (b) What is the probability that the random variable is between 2 and 3? (c) Plot the CDF.
  3. In Figure 8.7 it was assumed that the CDF function is monotonically increasing. That is, . Show why this is so using equation (13).
  4. Describe what is meant by *failure rate*, *failure function*, and *reliability*.
  5. Consider an integrated circuit that has . (a) Determine the mean time to failure. (b) Determine the reliability in 5, 10, 15, and 20 years.
  6. Consider a CD4001BC 2-Input Quad NOR gate (datasheet available in Appendix D). Also assume it is a glass-sealed dual inline package, 15 years in production, , the power dissipated in the application averages 10mW, it has B-1 quality, and that it is to be operated in laboratory equipment at an ambient temperature of 25°C. Use the MIL-HDBK-217F data in Appendix C to determine the MTTF and estimate its reliability in 25 years.
  7. Use the MIL-HDBK-217F data in Appendix C to estimate the reliability of a 32 bit CMOS microprocessor. Assume that it is used in a missile launcher, the ambient temperature is 120°C, that it has 64 pins, that it has been in production for 6 months, B-1 quality parts are used, and it is a non-hermetic DIP. Determine the MTTF and reliability for the microprocessor in 20 years. (Note: this is the same operating environment used for the BJT in Example 8.5.)
  8. Consider a 1N4001 diode (datasheet in Appendix D) that is to be operated in the switching circuit shown below. Also assume that the part quality is Lower, it is metallurgically bonded, and that it is to be used in an airborne inhabited cargo environment at an ambient temperature of 50°C. Use the MIL-HDBK-217F data in Appendix C to determine the MTTF and estimate its reliability in 25 years.



* 1. Use the MIL-HDBK-217F data in Appendix C to determine the reliability of the inverting op amp circuit, shown below, in 15 years. Assume that it is used in an automotive application (environmental factor = GM), the ambient operating temperature is 80OC, industrial quality parts are employed, and ¼ watt fixed composition resistors of the lowest quality are used. The datasheet for the LM741 op amp is in Appendix D. The LM741 is considered to be a linear microcircuit, comes in a dual inline package (DIP), contains 25 bipolar transistors, has S quality, and has been in production for well over 20 years.



* 1. Consider the circuit in Example 8.4. Assume that a heat sink is attached to the 2N3904 BJT (datasheet in Appendix D) and it has the following thermal resistance values  and . If the device is operated at an ambient temperature of 130°C, determine the maximum power dissipation of the BJT and its reliability in 50 years.
  2. Your company intends to design, manufacture, and market a new RAID (Redundant Array of Independent Disks) for network servers. The system must be able to store a total of 500GB of user data and must have a reliability of at least 95% in 10 years. In order to develop the RAID system, 20GB drives will be designed and utilized. To meet the requirement, you have decided to use a bank of 25 disks (25x20GB=500GB) and utilize a system redundancy of 4 (each of the 25 disks has a redundancy of 4). What must the reliability of the 20GB drive be in 10 years in order to meet the overall system reliability requirement?
  3.  has a failure probability of 2% and  has a failure probability of 3%. , , , and are identical redundant systems. Determine the required reliability of the redundant systems necessary for the overall system to have a reliability of 94%.



* 1. Consider the design of a triply-redundant majority voting system, with three binary inputs a, b, and c shown below. The inputs represent data from 3 independent sources from which the objective is to determine if the majority of the input bit values are logic level 0 or 1. Each majority circuit outputs the bit value that is in the majority of the inputs. The output of each majority circuit is fed into a resistor and LED (light emitting diode) network. The LEDs are lit if the output of the majority circuit is a logic 1, otherwise the LED is off. Ideally, all three LEDs are lit if the majority of inputs is 1, else they are all off. However, if part of the system fails, the LED readings may not be reliable, so a majority rules decision is used on the LEDs. The criterion used is that if two or more LEDs are on, then the majority of inputs are considered to be high, otherwise, the majority of inputs are considered to be low. Determine the probability of a false reading based upon this criterion if each component in the system (gate, resistor, or LED) has a reliability of 90%.

